

Amendments to the Claims

This listing of claims will replace all prior version and listings of the claims in the application.

Listing of Claims:

1. (Previously presented) A single USB interface comprising:  
a USB root hub host port coupled to a first USB bus; and  
a USB peripheral port coupled to a second USB bus wherein the USB peripheral port and the USB root hub host port are both active at the same time, wherein the USB root hub host port and the USB peripheral port are defined using predetermined signals.
2. (Cancelled)
3. (Previously presented) The interface of claim 1 wherein two connected devices utilizing the single USB interface can have a peer-to-peer connection via the USB root hub host port and the USB peripheral port.
4. (Previously presented) The interface of claim 1 wherein two connected devices utilizing the single USB interface can have a one-to-many relationship via either the USB root hub host port and/or the USB peripheral port.
5. (Previously presented) The USB interface of claim 1 wherein a device incorporating the USB interface is able to communicate with a second USB device using only one I/O USB port of the second device.
6. (Original) The interface of claim 1 wherein the predetermined signals comprise host differential data lines and peripheral differential data lines.
7. (Cancelled)
8. (Cancelled)
9. (Cancelled)

10. (Cancelled)

11. (Cancelled)

12. (Currently amended) A device comprising:

a processor; and

a single USB interface, the interface comprising a USB root hub host port coupled to a first USB bus; and

a USB peripheral port coupled to a second USB bus wherein the USB root hub host port and the USB peripheral port are defined using predetermined signals and wherein the USB root hub port and the USB peripheral port ~~port~~ are both active at the same time.

13. (Previously presented) The device of claim 12 wherein the single USB interface requires a connection to only one physical I/O port of a second device and this physical I/O port can be either a USB host port or a USB peripheral port which is defined using the predetermined signals.

14. (Original) The device of claim 12 wherein the predetermined signals comprise host differential data lines and peripheral differential data lines.